## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE **PATENT OPERATION**

In re the Application of: Kenichi WATANABE, et al.

Group Art Unit: 2814

Serial No.: 09/735,479

Examiner: PERALTA, Ginette

Filed: December 14, 2000

P.T.O. Confirmation No.: 8362

SEMICONDUCTOR DEVICE WITH DUAL DAMASCENE WIRING For:

Attorney Docket No.: 001620

Customer No.: 38834

## POWER TO FILE CORRECTED FORMAL DRAWINGS

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

The undersigned, attorney of record in the above-identified application, hereby instructs Jamie Locklear and Element Patent Drafting, to correct the drawings, as required and approved in such above-identified application and to file such corrected formal drawings in the United States Patent and Trademark Office in such application.

In the event that this paper and such formal drawings are not timely filed, Applicants respectfully petition for an appropriate extension of time. The fees for such an extension or any other fee which may be due with respect to this paper, may be charged to Deposit Account No. 50-2866.

Respectfully submitted,

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